

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	2	((("5258325") or ("5731945")).PN.	USPAT	OR	OFF	2007/11/29 15:46
S2	3503382	(integrat\$3 adj circuit\$1) or die\$1 or chip\$1 or semiconductor\$1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/04 22:16
S3	1276900	(signal with condition\$3) or (amplif\$4 adj circuit\$1) or amplifier\$1 or (power adj amplifier\$1) or (low adj noise adj amplifier\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/29 15:56
S4	986463	"PA" or "LNA"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/11/29 15:57
S5	2173816	S3 or S4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/04 22:19
S6	3103	(ancillary with circuit\$1) or (voltage adj resulator\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/04 22:32
S7	268660	(radio adj frequenc\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/29 16:01

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S8	418073	"RF"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/11/29 16:01
S9	544266	S7 or S8	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/12/04 22:20
S10	331486	signal with path\$1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/29 16:04
S11	723259	(substrate or wafer) with semiconductor\$1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/04 22:18
S12	931688	module\$1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/29 16:05
S13	6	S2 and S5 and S6 and S9 and S10 and S11 and S12	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/29 16:18
S14	20	S2 and S5 and S6 and S9 and S10 and S12	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/29 16:19

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S15	62602	first adj ((integrat\$3 adj circuit\$1) or die\$1 or chip\$1 or semiconductor\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/04 22:17
S16	58344	second adj ((integrat\$3 adj circuit\$1) or die\$1 or chip\$1 or semiconductor\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/04 22:17
S17	45686	S15 and S16	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/05 19:53
S18	723675	(substrate or wafer) with semiconductor\$1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/04 22:18
S19	1277433	(signal with condition\$3) or (amplif\$4 adj circuit\$1) or amplifier\$1 or (power adj amplifier\$1) or (low adj noise adj amplifier\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/04 22:19
S20	986776	"PA" or "LNA"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/12/04 22:19
S21	2174625	S19 or S20	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/04 22:19

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S22	268930	(radio adj frequenc\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/04 22:20
S23	418359	"RF"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/12/04 22:20
S24	544674	S22 or S23	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/04 22:20
S25	1947	S17 and S21 and S24	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/04 22:25
S26	3103	(ancillary with circuit\$1) or (voltage adj resulator\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/05 19:51
S27	5	S25 and S26	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/04 22:52
S28	0	("2007/0259639").URPN.	USPAT	OR	OFF	2007/12/04 22:33
S29	1	2005-322513.NRAN.	DERWENT	OR	OFF	2007/12/04 22:36
S30	0	("6806771").URPN.	USPAT	OR	OFF	2007/12/04 22:37

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S31	34	("3530395" "4250556" "4855685" "5068823" "5107146" "5196740" "5285169" "5311142" "5363055" "5416484" "5463349" "5510738" "5528179" "5554957" "5557234" "5574678" "5631606" "5680070" "5821776" "5877536" "5877612" "5912567" "5912583" "5952867" "5959871" "5966047" "5994926" "6014097" "6043709" "6111437" "6118266" "6140867" "6201429"). PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/12/04 22:37
S32	61082	ancillary or (voltage adj resulator\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/04 22:42
S33	14	S25 and S32	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/04 22:42
S34	57157	(ancillary with circuit\$1) or (voltage adj regulator\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/04 22:51
S35	58	S25 and S34	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/04 23:29
S36	12173	"BiCMOS"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/12/05 20:02

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S37	13	S35 and S36	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/04 23:29
S38	2	"7302247"	USPAT	OR	OFF	2007/12/05 15:09
S39	1	"7185244"	USPAT	OR	OFF	2007/12/05 18:59
S40	48562	(ancillary with circuit\$1) or ((voltage adj regulator\$1) and circuit\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/05 19:52
S41	8480	(ancillary with circuit\$1) or ((voltage adj regulator\$1) adj circuit\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/05 19:52
S42	62602	first adj ((integrat\$3 adj circuit\$1) or die\$1 or chip\$1 or semiconductor\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/05 19:53
S43	58344	second adj ((integrat\$3 adj circuit\$1) or die\$1 or chip\$1 or semiconductor\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/05 19:53
S44	45686	S42 and S43	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/05 20:01
S45	101	S41 and S44	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/05 20:03

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S46	12173	"BiCMOS"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/12/05 20:02
S47	3	S45 and S46	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/05 20:03
S48	90	("3713148" "3714540" "3859624" "4523128" "4536715" "4650981" "4675579" "4763075" "4785345" "4868647" "4924210" "4931867" "4937468" "4959631" "5095357" "5128729" "5168863" "5369666" "5384808" "5404545" "5442303" "5444740" "5448469" "5469098" "5555421" "5572179" "5591996" "5615091" "5615229" "5625265" "5650357" "5654984" "5663672" "5716323" "5774791" "5781071" "5781077" "5786763" "5789960" "5801602" "5812597" "5812598" "5845190" "5850436" "5864607" "5900683" "5907481" "5913817" "5926358" "5945728" "5952849" "5969590" "6049258" "6061009" "6069802" "6087882" "6114937" "6124756" "6137372" "6222922" "6232902" "6249171" "6262600" "6291907" "6307497" "6384763" "6389063" "6452519" "6525566" "6603807" "6611051" "6670861" "6728320" "6747522" "6833800" "6873065" "6903578" "6914547" "6922080" "6927662" "6940445" "6956727" "6967513" "6977522" "7016490" "7023372" "7053807" "7057491" "7075329" "7102388"). PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/12/05 23:52
S49	35	battery near2 (substitute or (back\$1up or backup)) and (regulator near4 voltage) same (second or additonal or two) and ((regulator near4 voltage) same (second or additional or two) same chip)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/18 20:02
S50	8	("5844853").URPN.	USPAT	OR	OFF	2007/12/18 20:28